

Amendments to the Specification:

Please change the title to: -- LIQUID CRYSTAL DISPLAY HAVING A MEMBER FOR PREVENTING ELECTRICAL SHORTING --.

Please replace paragraph [0006] with the following amended paragraph:

[0006] Generally, an LCD includes a thin film transistor (hereinafter abbreviated TFT) array on a lower substrate, an upper substrate having color filters, and a liquid crystal layer between those substrates. FIG. 1 shows a partial layout of a related art LCD, while FIG. 2 shows a cross-sectional view of that LCD along line [[A-A']] I-I' of FIG. 1. The lower substrate 101 includes gate lines 102 (only one is shown) and data lines 105 (only one is shown) that intersect to form a matrix of unit pixels. A TFT is formed at the gate and data line 102 and 105 crossing. That TFT is formed by stacking a gate electrode 102a, a semiconductor layer 104, a source electrode 105a, and a drain electrode 105b. A pixel electrode 107 in the unit pixel area electrically connects to the drain electrode 105b.

Please replace paragraph [0023] with the following amended paragraph:

[0023] FIG. 2 shows a cross-sectional view of the related art LCD along line [[A-A']] I-I' of FIG. 1;

Please replace paragraph [0025] with the following amended paragraph:

[0025] FIG. 4 shows a cross-sectional view of the LCD of FIG. 3 bisected along line [[A-A']] II-II' of FIG. 3;

Please replace paragraph [0030] with the following amended paragraph:

[0030] FIG. 3 shows an LCD according to the present invention, FIG. 4 shows a cross-sectional view of that LCD along line [[A-A']] II-II' of FIG. 3, and FIGS. 5A to FIGS. 5D show schematic layouts when fabricating an LCD according to the present invention. The inventive LCD includes a first substrate, called a TFT arrangement substrate, a second substrate, called a color filter substrate, and a liquid crystal layer between the first and the second substrates. The TFT arrangement substrate is explained in more detail in the following description.

Please replace paragraph [0031] with the following amended paragraph:

[0031] Referring now to FIG. 3 and FIG. 4, a TFT arrangement substrate in a liquid crystal display device according to the present invention includes a gate line 302, a gate electrode 302a, and a lower electrode 302c of a storage capacitor on a first substrate 301. A gate insulating layer 303 is then formed over the exposed surfaces, and a semiconductor layer 304 is formed on the gate insulating layer and over the gate electrode 302a. ~~A short prevention member 304a~~ Short-prevention members 304a and 304a' for preventing electric shorts [[is]] are formed simultaneously with the semiconductor layer 304. The short-prevention member 304a' is formed as an island.

Please replace paragraph [0039] with the following amended paragraph:

[0039] The short-prevention layer members 304a and 304a' induce opens in any metal residue 305d after etching the data line 305, the source/drain electrodes 305a/305b, and the upper electrode [[3305c]] 305c. Accordingly, electrical paths along the metal residue 305d are broken, thus preventing electrical shorts.